

RECEIPT

PATENT APPLICATION

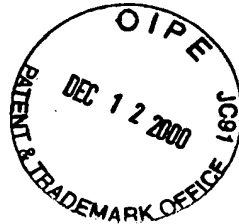
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#7

In re the Application of

Nobuaki HASHIMOTO

Application No.: 09/673,208



ATTN: Application Branch

Docket No.: 107281

Filed: October 13, 2000

For: SEMICONDUCTOR DEVICE, MOUNTING SUBSTRATE AND METHOD OF
MANUFACTURING MOUNTING SUBSTRATE, CIRCUIT BOARD, AND
ELECTRONIC INSTRUMENT

NOTIFICATION OF ERRORS IN FILING RECEIPT

Director of the U.S. Patent and Trademark Office
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Respectfully submitted,

James A. Oliff
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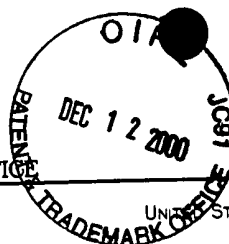
Date: December 12, 2000

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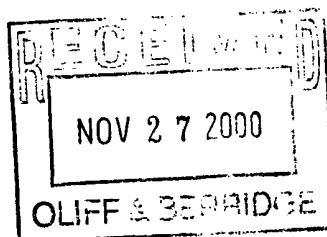


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| 09/673,208 | 10/13/2000 | 2811 | 3214 | 107281 | 12 | 53 | 14 |

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OC000000005567820

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Applicant(s)

Nobuaki Hashimoto, Nagano-ken, JAPAN;

Continuing Data as Claimed by Applicant

THIS APPLICATION IS A 371 OF PCT/JP00/00894 02/17/2000

Foreign Applications

JAPAN 11-039623(P) 02/18/1999

If Required, Foreign Filing License Granted 11/20/2000

Title

Semiconductor device, circuit board, method of manufacturing circuit board, and electronic device Mounting Substrate and method of manufacturing mounting substrate, Circuit Board, and electronic instrument.

Preliminary Class

257

Data entry by : ORDENEZ, MARTA

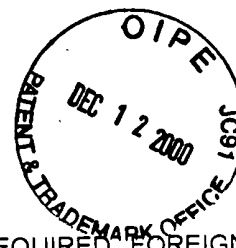
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